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PTO/SB/21 (01-03)  
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	Filing Date	February 20, 2004
	First Named Inventor	Kevin T. Look
	Examiner Name	Unknown
	Art Unit	2171
	Patent No.	
Attorney Docket Number	X-1462-2P US	

ENCLOSURES (check all that apply)		
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Firm or Customer Number	<b>24309</b> (Customer Number)	Reg. Number 51,959
Attn: Justin Liu		
Signature	<i>[Signature]</i>	
Date	August 16, 2006	
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X-1462-2P US

PATENT

IN THE UNITED STATES PATENT OFFICE

Applicants: Kevin T. Look et al.

Signee: Xilinx, Inc.

Title: "Regulating Unused/Inactive Resources in Programmable Logic Devices for Static Power Reduction"

Serial No.:	10/783,589	File Date:	02-20-2004
Examiner:	Unknown	Art Unit:	2171
Docket No.:	X-1462-2P US	Conf. No.:	1594

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the two (2) references listed in the attached Substitute for Form PTO-1449A/PT.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu  
Attorney for Applicants  
Reg. No. 51,959

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**Complete if Known**

<b>Application / Conf. No.</b>	10/783,589 / 1594
<b>Filing Date</b>	February 20, 2004
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<b>Art Unit</b>	2171
<b>Examiner Name</b>	Unknown
<b>Attorney Docket Number</b>	X-1462-2P US

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

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Examiner  
Signature

/Daniel Chang/

Date Considered

09/02/2008

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